

BIO DATA

Name: Dr.Bhagyalakshmi.N

Designation: Professor and HOD

Department: Electronics and Communication Engineering



EDUCATIONAL QUALIFICATIONS

Sl. No	Course	Branch/ Specialization	Name of the University/Board	Name of the Institute	Year of Degree Award/Pas
1	Ph.D	VLSI Design	Jain University, Bengaluru	Jain University, Bengaluru	2016
2	M.Tech	VLSI Design and Embedded Systems	VTU, Belgaum	UTL Technologies (VTU Extension Centre), Bengaluru	2007
3	B.E	Electronics & Communication	Bangalore University	Vivekananda Institute of Technology, Bengaluru	2001

TEACHING

Sl. No.	From	To	Name of the Institution	Designation	Subjects Taught
1	June 2019	Till Date	VKIT, Bengaluru	Professor and HOD	<ul style="list-style-type: none">➤ Verilog HDL➤ VLSI Design.➤ Basic Electronics.➤ Hardware Description Language.➤ Embedded System Design.➤ Antenna and Propagation.➤ Electronic Instrumentation.➤ Signals and Systems.➤ Electronic Circuits➤ Linear Integrated Circuits.
2	January 2016	May 2019	VKIT, Bengaluru	Associate Professor	
3	April 2010	December 2015	VKIT, Bengaluru	Assistant Professor	
4	August 2006	March 2010	VKIT, Bengaluru	Senior lecturer	
5	October 2002	July 2006	VKIT, Bengaluru	Lecturer	

AWARDS AND RECOGNITIONS

- **BEST PAPER AWARD:**

- Best Base paper award received for the paper entitled “Analysis of High performance FIR Filter using Improved Distributed Arithmetic” at the third International Conference on Information and Communication Engineering held on 27th October 2013, Bangalore.

- Reviewed Papers for IEEE sponsored International Conference on Recent advances in Electronics and Communication Technology (ICRAECT-2017)
- Certificate of Appreciation received for securing 100% result in the subject “Basic Electronics” (17ELN25) – Academic year 2017-18 (Even Sem).
- BOE Member for VTU during the year 2017 -2018
- “Woman Achiever Award” conferred on 08.03.2019 at “**Institution of Engineers**” (India), Karnataka State Centre, Bengaluru as part of the International Women’s Day Celebrations.

International Journals:

1. Mrs.Bhagyalakshmi.N, Dr.Rekha.K R, Dr.Nataraj.K R, “Analysis of high performance FIR filter using improved Distributed Arithmetic”, *International Journal of Recent Advances in Engineering & Technology (IJRAET)*, vol.1,no.2, pp.114-118, October 2013.
2. Mrs.Bhagyalakshmi.N, Dr.Rekha.K R, Dr.Nataraj.K R, “System Level Tools for Designing FIR Filter on FPGA”, *International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE)*, vol. 3, no. 2, pp.7148- 7154, February 2014 .
3. Mrs.Bhagyalakshmi.N, Dr.Rekha.K R, Dr.Nataraj.K R,“ Modified DA based FIR Filter in Multirate DSP systems on FPGA”, *International Journal on Recent and Innovation Trends in Computing and Communication (IJRITCC)*, vol. 4, no.5, pp 474-478, May 2016.

International Conferences:

1. Mrs.Bhagyalakshmi.N, Dr.Rekha.K R, Dr.Nataraj.K R, “Analysis of high performance FIR filter using improved Distributed Arithmetic”, *International Journal of Recent Advances in Engineering & Technology (IJRAET)*, vol.1,no.2,pp.114-118, October 2013.
2. Mrs.Bhagyalakshmi.N, Dr. K.R.Rekha, Dr. K.R.Nataraj, “Design and implementation of DA-Based Reconfigurable FIR Digital Filter on FPGA”, *2015 International Conference on Emerging Research in Electronics, Computer Science and Technology (ICERECT)*, pp. 214-217,17th- 19th December 2015(Indexed in IEEE Journal).
3. Mrs.Bhagyalakshmi.N, Dr. K.R.Rekha, Dr. K.R.Nataraj, “Reconfigurable FIR Filter on FPGA based on Modified DA for Multirate DSP systems”, *IEEE Sponsored 3rd International Conference on Electronics and Communication Systems (ICECS’16)*, vol.4, pp.2014-2020, 25th-26th February 2016.

National Conference:

1. Dr.Bhagyalakshmi.N, Mr.Anirudh.H.K, Mr.Rahul.G.V, “IOT based Camcorder Movie Piracy System”, *8th National Conference on Recent Trends in Electronics and Communication Engineering (NCRTEC- 18)*, pp.55-57, 9th May 2018.

Achievements:

- Project titled “GSM based Smart Sensor for industrial automatic fault diagnoser with voice facility” carried out by Final year UG students was sponsored by KSCST (Karnataka State Council for Technical Education) IISc, with an amount of Rs.4500/- in 2009.
- Paper Presentation on “Utilization of Back up server without user intervention” done by Final year UG students at Technical Fest conducted at MVJCE, Bangalore-April 2014.
- Project titled “IoT Based Camcorder Movie Piracy System” carried out by Final year UG students is sponsored with an amount of Rs.5000/- under FOSS (Free Open source Software) - VTU in 2018.
- Project titled “Raspberry Pi based Assistant Communication System for Blind Person” carried out by Final year UG students is sponsored with an amount of Rs.5000/- under FOSS (Free Open source Software)- VTU in 2018.
- Fund of Rs.20,000/- obtained for the proposal submitted for Entrepreneurship Awareness Camp (EAC) under DST-NIMAT Project 2018-19 in July 2018.
- Fund of Rs.40,000/- obtained for the proposal submitted for Entrepreneurship Awareness Camp (EAC) under DST-NIMAT Project 2019-20 in August 2019.

R & D Activities

- Guiding One Ph.D student:

1. **Mr.Syed Musadiq Illahi** on “A Novel Design and Development of a SV-UVM Based Smart Functional Verification Platform for SoCs”.

Membership in Professional Institutions/Bodies:

1. Life Member for ISTE
2. Life Member for MIE

Responsibilities Held:

- Ex-Staff – Secretary of “VKIT ALUMNI ASSOCIATION”.
- Presently Treasurer of “VKIT ALUMNI ASSOCIATION”.
- Governing Council Member (VKIT) -Faculty Representative since 2018.

AREAS OF INTEREST

Sl. No.	Theory Subjects of Interest to Teach	Areas of Research Interest
1	<ul style="list-style-type: none">➤ Embedded System Design➤ Microprocessors➤ Logic Design➤ Microcontrollers	<ul style="list-style-type: none">➤ VLSI Design➤ Embedded System

	<ul style="list-style-type: none"> ➤ Hardware Description Language ➤ VLSI Design ➤ Basic Electronics 	
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PROGRAMS/SEMINARS/CONFERENCES ATTENDED

- 1) Seminar on “Real Time Implementation of DSP Algorithms” – Feb 2007 at VKIT, Bangalore.
- 2) Seminar on “Recent trends in VLSI and Embedded system Design Technology”- April 2007 at GAT, Bangalore.
- 3) Workshop on “Fundamental of HDL”- Feb 2008 at VKIT, Bangalore.
- 4) Workshop on “Analog and Mixed Mode VLSI Design using Cadence Tool” – Feb 2009 at BNMIT, Bangalore.
- 5) Workshop on “DSP Architecture and Real Time Implementation of DSP Algorithms Using Texas Instruments DSP” – Aug 2009 at REVAITM, Bangalore.
- 6) Workshop on “Project oriented visual Basic based PIC Embedded system” – Apr 2010 at VKIT, Bangalore.
- 7) Faculty Development program on “Teaching and Learning Practices, Research Methodologies”- Jan 2011 at KSIT, Bangalore.
- 8) Workshop on “Experiential Learning with LABVIEW “by Texas Instruments – Jan-Feb 2011 at VKIT, Bangalore.
- 9) “Mission10X workshop” by Wipro-July 2011 at EWIT, Bangalore.
- 10) Workshop on “Explore VIN Research areas”-July 2012, at VKIT, Bangalore.
- 11) National conference on “Emerging Trends in Electronics, Communication and Computational Intelligence”- March 2013, at VKIT, Bangalore.
- 12) Faculty Development program on “Graphical system design in Engineering and Technology using NI’s LABVIEW”- Jan – Feb 2014 at SJBIT, Bangalore.
- 13) One Day Faculty Development Program on “Research Methodologies and LaTeX” at VKIT, Bangalore on 17th February 2014.
- 14) Two day Faculty Development Program on “Training the Trainers”, at VKIT, Bangalore – July 2014.
- 15) Two days Research Retreat Program at Jain University on 31st Jan-1st Feb 2015.
- 16) Two days Faculty Development Program by IBS, Bangalore, at VKIT– 20th and 21st April 2015.
- 17) One day program on “Mentor Forum Verification” on June 25th, 2015 conducted by Mentor Graphics, at Taj Hotel, Bangalore.
- 18) Six days Faculty Development Program on “Advanced Digital Electronics system using NI’s LABVIEW”, at SJBIT, Bangalore from 20th-25th January 2016.
- 19) One day Cadence University Conference on “Architecture to Silicon to systems: Academia

role in Electronic Design and R&D”, at Chancery Pavillion, Bangalore on 6th December 2016.

- 20) One day Seminar on “Mentor forum for Calibre”, at Leela Palace on 20th September 2017.
- 21) Five days Faculty Development Program on “Design of Embedded systems with ARM Cortex M3”, at BIT, Bengaluru from 16th - 20th January 2018.
- 22) One day workshop on “New model curriculum for First year as per Outcome Based Education (OBE) education including Course Outcomes (CO) and Blooms taxonomy”, at MSRIT, Bengaluru conducted by VTU on 14th May 2018.
- 23) One day Seminar on VLSI Design at SANLAB’s, Bengaluru on 11th January 2019.
- 24) One day Seminar on “NAAC –Upgraded Frame Work”, at JES, Bengaluru on 20th September 2019.

PERSONAL INFORMATION

DATE OF BIRTH :08.07.1979	AGE: 40 years	SEX: Female
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