

BIO DATA



Name: Dr. JYOTHILAKSHMI P

Designation: Chief Placement Officer/ Associate Professor

Department: training & Placement Department/ Electronics and Communication

EDUCATIONAL QUALIFICATIONS

Sl. No	Course	Branch/ Specialization	Name of the University/Board	Name of the Institute	Year of Passing
1	BE	Instrumentation Technology	Bangalore University	Bangalore Institute of Technology	1993
2	ME	Communication system	Anna University	Adhiyamman Institute of Technology	2009
3	Ph.D	Electronics	Jain University	Jain University	2016

TEACHING

Sl. No	From	To	Name of the Institution	Designation	Subjects Taught
1	July 1993	Sept 2007	Acharya Polytechnic	Senior Lecturer	Analog Electronics Microcontroller Biomedical Instrumentation
2	Aug 2009	Till date	Vivekananda Institute of Technology	Associate Professor/Chief Placement Officer	1.Basic Electronics 2.Analog Electronics 3.Electronic devices 4.Signals and system 5.Microprocessor 6.Microcontroller 7.Analog Communication 8.Digital communication 9.Operating System 10.Optical Fiber communication 11.Optical Networking 12.Digital signal Processing 13.DSP Architecture and Algorithms 14. Digital Switching System

AWARDS AND RECOGNITIONS:

Nil

AREAS OF INTEREST

Sl. No.	Theory Subjects of Interest to Teach	Areas of Research Interest
1	Image processing Human Computer Interface Pattern recognition	Image processing Human Computer Interface Pattern recognition

PROGRAMS/SEMINARS/CONFERENCES ATTENDED

- National Workshop on Advanced VLSI Technology – CS/IS Dept Acharya Institute of Technology. Course on DSP and Applications – EC Dept UVCE.
- Training on 8051 and PIC- ESA, Bangalore.
- Training program on Basics of Process Automation – SJP and ABB.
- Workshop on P C Servicing & networking – EC dept- M S Ramaiah Polytechnic. Mission 10X at East West Institute of technology.
- Visual Basic based PIC embedded system – EC/TE dept VKIT Work shop on Lab View – EC/TE dept VKIT
- Mission 10X – Personality development program- East West Institute of Technology
- Skill Development Program- Jain University
- Personality development program on LabView – SJBIT.
- Academia meet on “ Today and Tomorrow of EDSM Industry” organized by UTL.
- Five day FDP program on “ Design of Embedded Systems with ARM Cortex M3” organized by Telecommunication department, BIT.

PERSONAL INFORMATION

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