

BIO DATA

Name:Mr.Chethan T R

Designation:Asst Prof

Department:ECE



EDUCATIONAL QUALIFICATIONS

Sl. No	Course	Branch/ Specialization	Name of the University/ Board	Name of the Institute	Year of Passing
1	M Tech	VLSI Design and Embedded Systems	VTU	UTL VTU RC Bengaluru	2014
2	BE	Electronics and Communication	VTU	Govt Engineering College Ramanagaram	2011

TEACHING

Sl. No	From	To	Name of the Institution	Designation	Subjects Taught
1	03.09.2014	Till date	VKIT	Asst Proff	1.Fundamentals of CMOS VLSI 2.Management and Enterpreunership 3.Information Theory and Coding, 4.Network Security 5.Powerelectronics 6.Microelectronics,Global Systems For Mobile Communication, 7.Microelectro Mechanical Systems 8.Automotive Electronics 9.Digital Systems Design Using Verilog and 10.Powerelectronics and Instrumentation.

AWARDS AND RECOGNITIONS

BEST PAPER AWARD: Published a paper on a project “GSM Based Hardware Implementation of RFID Authentication System Using Actel FPGA” in International Journal of Engineering and Advanced Technology (IJEAT) in Volume-2,Issue-6,Agust 2013,pp 356- 359.

AREAS OF INTEREST

Sl. No	Theory Subjects of Interest to Teach	Areas of Research Interest
1	1.Fundamentals of CMOS VLSI 2.Information Theory and Coding, 3..Powerelectronics 4.Microelectronics 5.Microelectro Mechanical Systems 6.Automotive Electronics 7.Digital Systems Design Using Verilog and 8.Powerelectronics and Instrumentation.	VLSI and Embedded Systems

REASEARCH ACTIVITIES

GUIDING FINAL YEAR STUDENTS TO CONDUCT PROJECT WORK IN RESEARCH CENTER AT VKIT.

PROJECT TITLE: PERFORMANACE ANALYSIS OF 3-BIT 16T CMOS FULL ADDER.

PROGRAMS/SEMINARS/CONFERENCES ATTENDED

1. Participated in one day seminar Program on “Analog CMOS Layout” conducted by IEEE Student branch, VTU Extension Centre UTL Technologies Ltd Bengaluru.
2. Participated in seven days workshop program on “Analog and RF CMOS Circuits Design” conducted by M S Ramaiah Institute Of Technology Bengaluru.
3. Attended one day conference on “ Future Trends In VLSI Verification” conducted by Mentor Graphics.
4. Attended one day conference on “Cadence For Academia” conducted by Cadence.
5. Participated in two days workshop on “ Analog CMOS Circuits Design” conducted by department of ECE VKIT, Bengaluru.
6. Participated in one week workshop on “ Cortex-3 ARM Controller” conducted by department of ECE DSAT, Bengaluru.
7. Completed four months VLSI Physical Design course at Chipedge Pvt Ltd, Bengaluru.

PERSONAL INFORMATION

DATE OF BIRTH :15.05.1989	AGE: 30	SEX: MALE
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